Notice of Allowability	Application No.	Applicant(s)	Applicant(s)	
	00/70/ 077			
	09/721,677 <b>Examiner</b>	NORSKOV, SOREI	N	
	Lxammer	Art Unit		
	Jose H Alcala	2827		
All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313  1. This communication is responsive to after final amendment of the allowed claim(s) is/are 22-37 and 46-49.  3. The drawings filed on 16 January 2003 are accepted by the Acknowledgment is made of a claim for foreign priority and	(OR REMAINS) CLOSED or other appropriate comn <b>GHTS.</b> This application is and MPEP 1308.  tilled on 1/16/03.  Examiner.	in this application. If not includ nunication will be mailed in due subject to withdrawal from issu	led	
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority und</li> <li>a)  All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> </ul>		or (†).		
2. ☐ Certified copies of the priority documents have been received in Application No				
<ol> <li>Copies of the certified copies of the priority doc International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ol>	uments have been receive	ed in this national stage applica	ition from the	
5. Acknowledgment is made of a claim for domestic priority un	der 35 U.S.C. § 119(e) (to	a provisional application).		
(a) The translation of the foreign language provisional a	pplication has been receive	ed.		
6. Acknowledgment is made of a claim for domestic priority un	der 35 U.S.C. §§ 120 and	or 121.		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT of to the submitted of the su	his application. THIS THE	REE-MONTH PERIOD IS NOT	EXTENDABLE	
<ul> <li>8.  CORRECTED DRAWINGS must be submitted.</li> <li>(a)  including changes required by the Notice of Draftspers</li> <li>1)  hereto or 2)  to Paper No</li> <li>(b)  including changes required by the proposed drawing or</li> <li>(c)  including changes required by the attached Examiner's</li> </ul>	on's Patent Drawing Revie	ew ( PTO-948) attached ch has been approved by the E	Examiner. No	
Identifying indicia such as the application number (see 37 CFR 1.8 of each sheet. The drawings should be filed as a separate paper v	4(c)) should be written on t vith a transmittal letter addr	he drawings in the top margin (n essed to the Official Draftsperso	ot the back) on.	
9. DEPOSIT OF and/or INFORMATION about the deposattached Examiner's comment regarding REQUIREMENT FOR TH	t of BIOLOGICAL MAT IE DEPOSIT OF BIOLOGI	ERIAL must be submitted. N CAL MATERIAL.	lote the	
Attachment(s)				
<ul> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statements (PTO-1449), Paper No</li></ul>	4∏ Interviev 6⊠ Examine	f Informal Patent Application (F v Summary (PTO-413), Paper er's Amendment/Comment er's Statement of Reasons for A	No	

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## Notice of Allowance

## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the title: Replace title with: --Ground plane and capacitor plates for a semiconductor chip and method of making --.

## Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance: The prior art fails to teach, disclose, or suggest, either alone or in combination, at least on claims 22,48 and 49 a device having ground plane and capacitor plates for a semiconductor chip, and the method of making it comprising: at least one first capacitor plate provided within the semiconductor chip, and at least one second capacitor plate, the first and second capacitor plates being separated by a dielectric layer and capacitively coupled to each other via the dielectric layer, and the ground plane comprising at least one first conducting member and at least one electrically conducting via extending through a supporting member of the semiconductor chip and electrically coupled in series with the second capacitor plate; and wherein the second capacitor plate comprises a layer of electrically conductive glue which attaches the ground plane to the semiconductor chip.

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3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jose H Alcala whose telephone number is (703) 305-9844. The examiner can normally be reached on Monday to Friday.
- 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Talbott can be reached on (703) 305-9883. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 305-3431 for regular communications and (703) 305-3431 for After Final communications.
- 6. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

JHA March 21, 2003 SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

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